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1. A fast capability extension to a RISC architecture

Ghose, K.; Vasek, P.;

EUROMICRO 96, 'Beyond 2000: Hardware and Software Design Strategies', I

the 22nd EUROMICRO Conference

2-5 Sept. 1996 Page(s):606 - 613

Digital Object Identifier 10.1109/EURMIC.1996.546488

AbstractPlus | Full Text: PDF(732 KB) IEEE CNF

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2. Predicting real-time behaviour for data flow computations

Jonsson, J.; Olsson, A.; Vasell, J.;

Real-Time Systems, 1995. Proceedings., Seventh Euromicro Workshop on

14-16 June 1995 Page(s):270 - 275

Digital Object Identifier 10.1109/EMWRTS.1995.514321

AbstractPlus | Full Text: PDF(540 KB) IEEE CNF

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3. N-MAP: a virtual processor discrete event simulation tool for performanc the CAPSE environment

Ferscha, A.; Johnson, J.;

System Sciences, 1995. Proceedings of the Twenty-Eighth Hawaii International

Volume 2, 3-6 Jan. 1995 Page(s):276 - 285 vol.2

Digital Object Identifier 10.1109/HICSS.1995.375450

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4. Efficient collective data distribution in all-port wormhole-routed hypercul

Robinson, D.F.; Judd, D.; McKinley, P.K.; Cheng, B.C.;

Supercomputing '93. Proceedings

15-19 Nov. 1993 Page(s):792 - 801

Digital Object Identifier 10.1109/SUPERC.1993.1263537

AbstractPlus | Full Text: PDF(624 KB) IEEE CNF

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5. Energy efficient comparators for superscalar datapaths

Ponomarev, D.V.; Kucuk, G.; Ergin, O.; Ghose, K.;

Computers, IEEE Transactions on

Volume 53, Issue 7, July 2004 Page(s):892 - 904

Digital Object Identifier 10.1109/TC.2004.29

AbstractPlus | References | Full Text: PDF(1112 KB) | IEEE JNL Rights and Permissions

6. Architecture of the Atlas chip-multiprocessor: dynamically parallelizing in applications

Codrescu, L.; Wills, D.S.; Meindl, J.;

Computers, IEEE Transactions on

Volume 50, Issue 1, Jan. 2001 Page(s):67 - 82

Digital Object Identifier 10.1109/12.902753

AbstractPlus | References | Full Text: PDF(4260 KB) | IEEE JNL

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7. Scheduling DAG's for asynchronous multiprocessor execution

Malloy, B.A.; Lloyd, E.L.; Soffa, M.L.;

Parallel and Distributed Systems, IEEE Transactions on

Volume 5, Issue 5, May 1994 Page(s):498 - 508

Digital Object Identifier 10.1109/71.282560

AbstractPlus | Full Text: PDF(1180 KB) IEEE JNL

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8. Evolution of Processor microcode

Jackson, D.;

Evolutionary Computation, IEEE Transactions on

Volume 9, Issue 1, Feb 2005 Page(s):44 - 54

Digital Object Identifier 10.1109/TEVC.2004.837922

AbstractPlus | Full Text: PDF(408 KB) IEEE JNL

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9. Program Graph Structuring for Execution in Dynamic SMP Clusters Using

Masko, L.; Mounie, G.; Trystram, D.; Tudruj, M.;

Parallel Computing in Electrical Engineering, 2006. PAR ELEC 2006. International Computing in Electrical Engineering, 2006.

2006 Page(s):95 - 100

Digital Object Identifier 10.1109/PARELEC.2006.69

AbstractPlus | Full Text: PDF(700 KB) IEEE CNF

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10. Fast Matrix Multiplication in Dynamic SMP Clusters with Communication Systems on Chip Technology

Masko, L.; Tudruj, M.;

Parallel Computing in Electrical Engineering, 2006. PAR ELEC 2006. International Electrical Engineering, 2006.

2006 Page(s):77 - 82

Digital Object Identifier 10.1109/PARELEC.2006.38

AbstractPlus | Full Text: PDF(189 KB) IEEE CNF

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11. SimGate: Full-System, Cycle-Close Simulation of the Stargate Sensor Ne Intermediate Node

Ye Wen; Gurun, S.; Chohan, N.; Wolski, R.; Krintz, C.;

Embedded Computer Systems: Architectures, Modeling and Simulation, 2006

Conference on

July 2006 Page(s):129 - 136

Digital Object Identifier 10.1109/ICSAMOS.2006.300819

AbstractPlus | Full Text: PDF(120 KB) IEEE CNF

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12. QoS-aware replanning of composite Web services Canfora, G.; Di Penta, M.; Esposito, R.; Villani, M.L.; Web Services, 2005. ICWS 2005. Proceedings. 2005 IEEE International Confe 11-15 July 2005 Page(s):121 - 129 vol.1 Digital Object Identifier 10.1109/ICWS.2005.96 AbstractPlus | Full Text: PDF(456 KB) IEEE CNF Rights and Permissions 13. From test count to code coverage using the Lognormal Failure Rate Г Gokhale, S.S.; Mullen, R.E.; Software Reliability Engineering, 2004. ISSRE 2004. 15th International Sympo 2-5 Nov. 2004 Page(s):295 - 305 Digital Object Identifier 10.1109/ISSRE.2004.20 AbstractPlus | Full Text: PDF(184 KB) | IEEE CNF Rights and Permissions 14. Optimizing the execution of independent multi-version programs Malhotra, M.; Digital Avionics Systems Conference, 2003. DASC '03. The 22nd Volume 2, 12-16 Oct. 2003 Page(s):10.A.4 - 10.1-7 vol.2 Digital Object Identifier 10.1109/DASC.2003.1245930 AbstractPlus | Full Text: PDF(475 KB) IEEE CNF Rights and Permissions 15. Power efficient comparators for long arguments in superscalar processo Г Ponomarev, D.; Kucuk, G.; Ergin, O.; Ghose, K.; Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 200 Symposium on 25-27 Aug. 2003 Page(s):378 - 383 Digital Object Identifier 10.1109/LPE.2003.1231928 AbstractPlus | Full Text: PDF(691 KB) | IEEE CNF Rights and Permissions 16. Scalability of parallel simulation cloning Hybinette, M.; Fujimoto, R.M.; Simulation Symposium, 2002. Proceedings. 35th Annual 14-18 April 2002 Page(s):275 - 282 AbstractPlus | Full Text: PDF(239 KB) IEEE CNF Rights and Permissions 17. VPPB-a visualization and performance prediction tool for multithreaded \$ Г Broberg, M.; Lundberg, L.; Grahn, H.; Parallel Processing Symposium, 1998. 1998 IPPS/SPDP. Proceedings of the I International...and Symposium on Parallel and Distributed Processing 1998 30 March-3 April 1998 Page(s):770 - 776 Digital Object Identifier 10.1109/IPPS.1998.670014 AbstractPlus | Full Text: PDF(868 KB) IEEE CNF Rights and Permissions 18. Distributed simulation of parallel executions Ricciulli, L.; Lincoln, P.; Meseguer, J.; Simulation Symposium, 1996. Proceedings of the 29th Annual 8-11 April 1996 Page(s):15 - 24 Digital Object Identifier 10.1109/SIMSYM.1996.492148 AbstractPlus | Full Text: PDF(888 KB) IEEE CNF Rights and Permissions 19. Analysis of algorithmic structures with heterogeneous tasks Wilson, L.F.;

Computers and Communications, 1996., Conference Proceedings of the 1996 Annual International Phoenix Conference on 27-29 March 1996 Page(s):253 - 261 Digital Object Identifier 10.1109/PCCC.1996.493642 AbstractPlus | Full Text: PDF(952 KB) | IEEE CNF Rights and Permissions

20. A visual environment for designing and simulating execution of process Norton, C.D.; Glinert, E.P.;

Visual Languages, 1990., Proceedings of the 1990 IEEE Workshop on 4-6 Oct. 1990 Page(s):227 - 232

Digital Object Identifier 10.1109/WVL.1990.128411

AbstractPlus | Full Text: PDF(428 KB) | IEEE CNF

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Г

21. Analysis tool for concurrent processing computer systems

Jones, R.L.; Stoughton, J.W.; Mielke, R.R.; Southeastcon '91., IEEE Proceedings of 7-10 April 1991 Page(s):620 - 625 vol.2 Digital Object Identifier 10.1109/SECON.1991.147830

AbstractPlus | Full Text: PDF(596 KB) IEEE CNF

Rights and Permissions

22. A fine grained approach to scheduling asynchronous multiprocessors

Malloy, B.; Lloyd, E.L.; Soffa, M.L.;

Computing and Information, 1992. Proceedings. ICCI '92., Fourth International

28-30 May 1992 Page(s):139 - 142

Digital Object Identifier 10.1109/ICCI.1992.227686

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> 4. The impact of instruction-level parallelism on multiprocessor performanc Г methodology

> > Pai, V.S.; Ranganathan, P.; Adve, S.V.;

High-Performance Computer Architecture, 1997., Third International Symposiu 1-5 Feb. 1997 Page(s):72 - 83

Digital Object Identifier 10.1109/HPCA.1997.569611

AbstractPlus | Full Text: PDF(1248 KB) IEEE CNF

Rights and Permissions

5. Towards a thread-based parallel direct execution simulator

Dickens, P.; Haines, M.; Mehrotra, P.; Nicol, D.;

System Sciences, 1996., Proceedings of the Twenty-Ninth Hawaii Internationa Volume 1, 3-6 Jan. 1996 Page(s):424 - 432 vol.1

Digital Object Identifier 10.1109/HICSS.1996.495490

AbstractPlus | Full Text: PDF(920 KB) IEEE CNF Rights and Permissions

6. A distributed modeling approach for simulation and verification of digital Ghosh, S.;

Circuits and Systems, IEEE Transactions on

Volume 34, Issue 10, Oct 1987 Page(s):1171 - 1181

AbstractPlus | Full Text: PDF(1480 KB) | IEEE JNL

Rights and Permissions

7. Queuing simulation model for multiprocessor systems

Thin-Fong Tsuei; Yamamoto, W.;

Computer

Volume 36, Issue 2, Feb. 2003 Page(s):58 - 64

Digital Object Identifier 10.1109/MC.2003.1178049

AbstractPlus | References | Full Text: PDF(275 KB) | IEEE JNL

Rights and Permissions

8. MADESE: A Simulation Environment for Mobile Agent

Xuhui Li; Jiannong Cao; Yanxiang He; Yifeng Chen;

Computer and Information Technology, 2006. CIT '06. The Sixth IEEE Internat

Sept. 2006 Page(s):86 - 86

Digital Object Identifier 10.1109/CIT.2006.116

AbstractPlus | Full Text: PDF(138 KB) IEEE CNF

Rights and Permissions

9. EMPS: an environment for memory performance studies

Hollingsworth, J.K.; Snavely, A.; Sbaraglia, S.; Ekanadham, K.;

Parallel and Distributed Processing Symposium, 2005. Proceedings, 19th IEEE

4-8 April 2005 Page(s):8 pp.

Digital Object Identifier 10.1109/IPDPS.2005.196

AbstractPlus | Full Text: PDF(120 KB) IEEE CNF

Rights and Permissions

10. An integrated approach for real-time system design

Lichen Zhang; Peijiang Yuan;

Communications, Computers and Signal Processing, 1999 IEEE Pacific Rim C

22-24 Aug. 1999 Page(s):63 - 66

Digital Object Identifier 10.1109/PACRIM.1999.799478

AbstractPlus | Full Text: PDF(400 KB) | IEEE CNF

Rights and Permissions

11. DISCOE: distributed design and analysis to preserve intellectual property Г

Dieckman, D.; Martin, D.E.; Wilsey, P.A.;

Information Technology Conference, 1998. IEEE

1-3 Sept. 1998 Page(s):57 - 60

Digital Object Identifier 10.1109/IT.1998.713381

AbstractPlus | Full Text: PDF(356 KB) | IEEE CNF

Rights and Permissions

12. Instruction level analytic prediction of parallel CPU architecture performa

De Gloria, A.; Ancarani, F.; Bellotti, F.; Olivieri, M.;

Intelligent Information Systems, 1997. IIS '97. Proceedings

8-10 Dec. 1997 Page(s):530 - 534

Digital Object Identifier 10.1109/IIS.1997.645379

AbstractPlus | Full Text: PDF(376 KB) IEEE CNF

Rights and Permissions

13. Integrating different tools to develop multiprocessor real-time systems Lichen Zhang; Communications, Computers and Signal Processing, 1997, '10 Years PACRIM Networking the Pacific Rim'. 1997 IEEE Pacific Rim Conference on Volume 2, 20-22 Aug. 1997 Page(s):964 - 967 vol.2 Digital Object Identifier 10.1109/PACRIM.1997.620420 AbstractPlus | Full Text: PDF(440 KB) | IEEE CNF Rights and Permissions 14. A visual environment for designing and simulating execution of process Г Norton, C.D.; Glinert, E.P.; Visual Languages, 1990., Proceedings of the 1990 IEEE Workshop on 4-6 Oct. 1990 Page(s):227 - 232 Digital Object Identifier 10.1109/WVL.1990.128411 AbstractPlus | Full Text: PDF(428 KB) | IEEE CNF Rights and Permissions 15. Simulation of multiprocessor robot controllers Astraudo, C.; Borrelly, J.-J.; Robotics and Automation, 1992. Proceedings., 1992 IEEE International Conference 12-14 May 1992 Page(s):573 - 578 vol.1 Digital Object Identifier 10.1109/ROBOT.1992.220230 AbstractPlus | Full Text: PDF(480 KB) | IEEE CNF Rights and Permissions 16. A Test Data Generation Tool for Unit Testing of C Programs Zhongxing Xu; Jian Zhang; Quality Software, 2006. QSIC 2006. Sixth International Conference on Oct. 2006 Page(s):107 - 116 Digital Object Identifier 10.1109/QSIC.2006.7 AbstractPlus | Full Text: PDF(129 KB) | IEEE CNF Rights and Permissions 17. Comparing multinomial and k-means clustering for SimPoint Hamerly, G.; Perelman, E.; Calder, B.; Performance Analysis of Systems and Software, 2006 IEEE International Sym 19-21 March 2006 Page(s):131 - 142 Digital Object Identifier 10.1109/ISPASS.2006.1620798 AbstractPlus | Full Text: PDF(252 KB) IEEE CNF Rights and Permissions 18. VHDLA flexible simulator for distributed algorithms Marcelin-Jlmenez, R.; Esquivel-Villafana, R.; Rajsbaum, S.; Computer Science, 2003. ENC 2003. Proceedings of the Fourth Mexican Inter Conference on 8-12 Sept. 2003 Page(s):176 - 181 Digital Object Identifier 10.1109/ENC.2003.1232892 AbstractPlus | Full Text: PDF(366 KB) | IEEE CNF Rights and Permissions 19. A visual authoring environment for prototyping multimedia presentations Gaggi, O.; Celentano, A.; Multimedia Software Engineering, 2002. Proceedings. Fourth International Syr 11-13 Dec. 2002 Page(s):206 - 213 Digital Object Identifier 10.1109/MMSE.2002.1181614 AbstractPlus | Full Text: PDF(727 KB) IEEE CNF Rights and Permissions

20. A system to automate the generation of program variants for industrial re Freund, E.; Luedemann-Ravit, B.; Intelligent Robots and System, 2002, IEEE/RSJ International Conference on Volume 2, 30 Sept.-5 Oct. 2002 Page(s):1856 - 1861 vol.2 Digital Object Identifier 10.1109/IRDS.2002.1044026 AbstractPlus | Full Text: PDF(858 KB) | IEEE CNF Rights and Permissions 21. Rule termination analysis investigating the interaction between transaction Montesi, D.; Bertino, E.; Bagnato, M.; Dearnley, P.; Database Engineering and Applications Symposium, 2002. Proceedings. Inter 17-19 July 2002 Page(s):285 - 294 Digital Object Identifier 10.1109/IDEAS.2002.1029681 AbstractPlus | Full Text: PDF(659 KB) IEEE CNF Rights and Permissions 22. Automatic translation of a timed process algebra into dynamic state grap Pardo, J.J.; Valero, V.; Cuartero, F.; Cazorla, D.; Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific 4-7 Dec. 2001 Page(s):63 - 70 AbstractPlus | Full Text: PDF(835 KB) | IEEE CNF Rights and Permissions 23. Multiprocessor memory reference generation using Cerberus Rothman, J.B.; Smith, A.J.; Modeling, Analysis and Simulation of Computer and Telecommunication Syste Proceedings. 7th International Symposium on 24-28 Oct. 1999 Page(s):278 - 287 Digital Object Identifier 10.1109/MASCOT.1999.805065 AbstractPlus | Full Text: PDF(172 KB) | IEEE CNF Rights and Permissions 24. A tool for automated system analysis based on modular specifications Morzenti, A.; Pietro, P.S.; Morasca, S.; Automated Software Engineering, 1998. Proceedings. 13th IEEE International 13-16 Oct. 1998 Page(s):2 - 11 Digital Object Identifier 10.1109/ASE.1998.732560 AbstractPlus | Full Text: PDF(104 KB) IEEE CNF Rights and Permissions 25. Distributed simulation of parallel executions Ricciulli, L.; Lincoln, P.; Meseguer, J.; Simulation Symposium, 1996. Proceedings of the 29th Annual 8-11 April 1996 Page(s):15 - 24 Digital Object Identifier 10.1109/SIMSYM.1996.492148 AbstractPlus | Full Text: PDF(888 KB) | IEEE CNF

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SASI enforcement of security policies: a retrospective Ulfar Erlingsson, Fred B. Schneider

September 1999 Proceedings of the 1999 workshop on New security paradigms NSPW

Publisher: ACM Press

Full text available: 7 pdf(862.14 KB) Additional Information: full citation, references, citings, index terms

2 Enforceable security policies

Fred B. Schneider

February 2000 ACM Transactions on Information and System Security (TISSEC), Volume 3 Issue 1

Publisher: ACM Press

Full text available: pdf(148.24 KB)

Additional Information: full citation, abstract, references, citings, index terms, review

A precise characterization is given for the class of security policies enforceable with mechanisms that work by monitoring system execution, and automata are introduced for specifying exactly that class of security policies. Techniques to enforce security policies specified by such automata are also discussed.

Keywords: EM security policies, SASI, inlined reference monitors, proof carrying code, safety properties, security automata, security policies

3 Software Engineering for Secure Systems (SESS) --- Building Trustworthy

Applications: A framework for testing security mechanisms for program-based attacks Ben Breech, Lori Pollock

May 2005 ACM SIGSOFT Software Engineering Notes , Proceedings of the 2005 workshop on Software engineering for secure systems—building trustworthy applications SESS '05, Volume 30 Issue 4

Publisher: ACM Press

Full text available: 🔂 pdf(170.09 KB) Additional Information: full citation, abstract, references, index terms

Program vulnerabilities leave organizations open to malicious attacks that can result in severe damage to company finances, resources, consumer privacy, and data. Engineering applications and systems so that vulnerabilities do not exist would be the best solution, but this strategy may be impractical due to fiscal constraints or inadequate knowledge.

Therefore, a variety of program and system-based solutions have been proposed to deal with vulnerabilities in a manageable way. Unfortunately, prop ...

4 <u>Military applications: Security issues in high level architecture based distributed</u> simulation

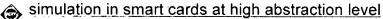
Asa Elkins, Jeffery W. Wilson, Denis Gracanin

December 2001 Proceedings of the 33nd conference on Winter simulation WSC '01 Publisher: IEEE Computer Society

Full text available: pdf(262.26 KB) Additional Information: full citation, abstract, references, index terms

The United States Department of Defense (DoD) has, over the past several years, emphasized the need to employ simulation based acquisition (SBA) in engineering and development. Distributed simulation introduces an information assurance challenge and details of a simulation must be guarded from unauthorized access. The High Level Architecture (HLA) and its Run-Time Interface (RTI) do not define support of mandatory access controls (MACs) or discretionary access controls (DACs) required to provide ...

5 Compilation and power: Power consumption profile analysis for security attack



K. Rothbart, U. Neffe, Ch. Steger, R. Weiss, E. Rieger, A. Muehlberger September 2005 Proceedings of the 5th ACM international conference on Embedded software EMSOFT '05

Publisher: ACM Press

Full text available: pdf(273.40 KB) Additional Information: full citation, abstract, references, index terms

Smart cards are embedded systems which are used in an increasing number of secure applications. As they store and deal with confidential and secret data many attacks are performed on these cards to reveal this private information. Consequently, the security demands on smart cards are very high. It is mandatory to evaluate the security of the design but this is performed often very late in the design process or when the chip has already been manufactured. This paper presents a hierarchical securi ...

Keywords: SystemC, analysis, attack, embedded security, fault injection, power profile, simulation, smart card

SOAr-DSGrid: Service-Oriented Architecture for Distributed Simulation on the Grid Xinjun Chen, Wentong Cai, Stephen J. Turner, Yong Wang

May 2006 Proceedings of the 20th Workshop on Principles of Advanced and Distributed Simulation PADS '06

Publisher: IEEE Computer Society

Full text available: Tpdf(357.77 KB) Additional Information: full citation, abstract, index terms

Simulation is a low cost alternative to experimentation on real-world physical systems. Grid technology enables coordinated use of and secure access to distributed computing resources and data sources. The service-oriented architecture (SOA) is an ideal paradigm for next generation computing. The loose coupling among services in the SOA relieves service consumers from detailed knowledge of implementation, implementation language, and execution platform of the services to be consumed. In this pap ...

7 New techniques for security and reliability enhancement in embedded systems:

© Current flattening in software and hardware for security applications Radu Muresan, Catherine Gebotys

September 2004 Proceedings of the 2nd IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis CODES+ISSS '04

Publisher: ACM Press

Full text available: ndf(344.68 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a new current flattening technique applicable in software and hardware. This technique is important in embedded cryptosystems since power analysis attacks (that make use of the current variation dependency on data and program) compromise the security of the system. The technique flattens the current internally by exploiting current consumption differences at the instruction level. Code transformations supporting current variation reductions due to program dependencies are pre ...

Keywords: current flattening, hardware architecture, power analysis attacks

8 Manufacturing applications: production management IV: Database-intensive process simulation at the Y-12 national security complex



Reid Kress, Karen Bills, Jack Dixon, Richard Rinehart

December 2006 Proceedings of the 38th conference on Winter simulation WSC '06

Publisher: Winter Simulation Conference

Full text available: pdf(588.52 KB) Additional Information: full citation, abstract, references

The NNSA's Y-12 National Security Complex is a manufacturing facility operated by BWXT Y-12. Y-12's missions include ensuring the US' nuclear weapons deterrent, storing nuclear materials, and fueling US naval reactors. As a consequence of these missions, Y-12 makes dozens of products, having hundreds of parts, each with many different process steps associated with manufacturing components, building sub-assemblies, or assembling final products. Y-12 also disassembles weapon components to support ...

9 Session 7A: Bounded-concurrent secure multi-party computation with a dishonest



majority

Rafael Pass

June 2004 Proceedings of the thirty-sixth annual ACM symposium on Theory of computing STOC '04

Publisher: ACM Press

Full text available: pdf(246.91 KB)

Additional Information: full citation, abstract, references, citings, index terms

We show how to securely realize any multi-party functionality in a way that preserves security under an a-priori bounded number of concurrent executions, regardless of the number of corrupted parties. Previous protocols for the above task either rely on set-up assumptions such as a Common Reference String, or require an honest majority. Our constructions are in the plain model and rely on standard intractability assumptions (enhanced trapdoor permutations and collision resistant hash functions). ...

Keywords: concurrent composition, constant-round protocols, secure multi-party computation, simulation-sound zero-knowledge

10 Session 12A: Bounded-concurrent secure two-party computation without setup



assumptions

Yehuda Lindell

June 2003 Proceedings of the thirty-fifth annual ACM symposium on Theory of computing STOC '03

Publisher: ACM Press

Full text available: pdf(338.32 KB) Additional Information: full citation, abstract, references, citings, index terms

In this paper we study the feasibility of obtaining protocols for general two-party computation that remain secure under concurrent composition. (A general protocol can be used for obtaining secure computation of any functionality.) We consider a scenario where no trusted setup is assumed (and so, for example, there is no common reference string available to the parties); we call this the "plain model". We present both negative and positive results for this model. Specifically, we show th ...

Keywords: protocol composition, secure computation

11 A network technique to achieve program and data security with nominal communications overhead

J. R. Driscoll, H. N. Srinidhi, T. S. Chesser

November 1986 Proceedings of 1986 ACM Fall joint computer conference ACM '86

Publisher: IEEE Computer Society Press

Full text available: 7 pdf(731.27 KB) Additional Information: full citation, references, index terms

12 On the composition of authenticated Byzantine Agreement

Yehuda Lindell, Anna Lysyanskaya, Tal Rabin

November 2006 Journal of the ACM (JACM), Volume 53 Issue 6

Publisher: ACM Press

Full text available: pdf(415.67 KB) Additional Information: full citation, abstract, references, index terms

A fundamental problem of distributed computing is that of simulating a secure broadcast channel, within the setting of a point-to-point network. This problem is known as Byzantine Agreement (or Generals) and has been the focus of much research. Lamport et al. [1982] showed that in order to achieve Byzantine Agreement in the plain model, more than two thirds of the participating parties must be honest. They further showed that by augmenting the network with a public-key infrastructure for digital ...

Keywords: Authenticated Byzantine Agreement, lower bounds, protocol composition, randomized protocols

13 Anonymity systems & formal method: Specifying and analyzing security automata

using CSP-OZ

David Basin, Ernst-Ruediger Olderog, Paul E. Sevinc

March 2007 Proceedings of the 2nd ACM symposium on Information, computer and communications security ASIACCS '07

Publisher: ACM Press

Full text available: pdf(305.81 KB) Additional Information: full citation, abstract, references, index terms

Security automata are a variant of Büchi automata used to specify security policies that can be enforced by monitoring system execution. In this paper, we propose using CSP-OZ, a specification language combining Communicating Sequential Processes (CSP) and Object-Z (OZ), to specify security automata, formalize their combination with target systems, and analyze the security of the resulting system specifications. We provide theoretical results relating CSP-OZ specifications and security auto ...

Keywords: CSP-OZ, security automata

14 Session 7A: New notions of security: achieving universal composability without

trusted setup

Manoj Prabhakaran, Amit Sahai

June 2004 Proceedings of the thirty-sixth annual ACM symposium on Theory of

computing STOC '04

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(253.45 KB) terms

We propose a modification to the framework of Universally Composable (UC) security [3]. Our new notion involves comparing the real protocol execution with an ideal execution involving ideal functionalities (just as in UC-security), but allowing the environment and adversary access to some super-polynomial computational power. We argue the meaningfulness of the new notion, which in particular subsumes many of the traditional notions of security. We generalize the Universal Composition theorem of ...

Keywords: environmental security, general composition, generalized environmental security, secure multi-party computation, secure protocols, simulation, universal composability

15 Workshop on architectural support for security and anti-virus (WASSA): ChipLock:





support for secure microarchitectures Taeho Kgil, Laura Falk, Trevor Mudge

March 2005 ACM SIGARCH Computer Architecture News, Volume 33 Issue 1

Publisher: ACM Press

Full text available: pdf(256.52 KB) Additional Information: full citation, abstract, references, index terms

The increasing need for security has caused system designers to consider placing some security support directly at the hardware level. In fact, this is starting to emerge as an important consideration in processor design, because the performance overhead of supporting security in hardware is usually significantly lower than a complete software solution. In this paper, we investigate integrating some security support into hardware. We show that security support can be added at some acceptable cos ...

16 Development of processors and communication networks for embedded systems:



System design methodologies for a wireless security processing platform Srivaths Ravi, Anand Raghunathan, Nachiketh Potlapally, Murugan Sankaradass June 2002 Proceedings of the 39th conference on Design automation DAC '02 **Publisher: ACM Press**

Full text available: pdf(207.37 KB)

Additional Information: full citation, abstract, references, citings, index

Security protocols are critical to enabling the growth of a wide range of wireless data services and applications. However, they impose a high computational burden that is mismatched with the modest processing capabilities and battery resources available on wireless clients. Bridging the security processing gap, while retaining sufficient programmability in order to support a wide range of current and future security protocol standards, requires the use of novel system architectures and design m ...

Keywords: 3DES, AES, DES, IPSec, RSA, SSL, decryption, design methodology, embedded system, encryption, handset, performance, platform, security, security processing, system architecture, wireless

17 Security: SECA: security-enhanced communication architecture

Joel Coburn, Srivaths Ravi, Anand Raghunathan, Srimat Chakradhar September 2005 Proceedings of the 2005 international conference on Compilers, architectures and synthesis for embedded systems CASES '05

Publisher: ACM Press

Full text available:

Additional Information:

7 pdf(396.53 KB)

full citation, abstract, references, index terms

In this work, we propose and investigate the idea of enhancing a System-on-Chip (SoC) communication architecture (the fabric that integrates system components and carries the communication traffic between them) to facilitate higher security. We observe that a wide range of common security attacks are manifested as abnormalities in the system-level communication traffic. Therefore, the communication architecture, with its global systemlevel visibility, can be used to detect them. The communicati ...

Keywords: AMBA Bus, access control, architecture, attacks, bus, communication, digital rights management (DRM), intrusion detection, security, security-aware design, small embedded systems, system-on-chip (SoC)

18 Frontmatter (TOC, Letters, Election results, Software Reliability Resources!,

© Computing Curricula 2004 and the Software Engineering Volume SE2004, Software Reuse Research, ICSE 2005 Forward)

July 2005 ACM SIGSOFT Software Engineering Notes, Volume 30 Issue 4

Publisher: ACM Press

Full text available: pdf(6.19 MB) Additional Information: full citation, index terms

19 Session 14A: Concurrent general composition of secure protocols in the timing model Yael Tauman Kalai, Yehuda Lindell, Manoj Prabhakaran



May 2005 Proceedings of the thirty-seventh annual ACM symposium on Theory of computing STOC '05

Publisher: ACM Press

Full text available: ndf(263.20 KB) Additional Information: full citation, abstract, references, index terms

In the setting of secure multiparty computation, a set of mutually distrustful parties wish to jointly compute some function of their input (i.e., they wish to securely carry out some distributed task). %The joint computation should be such that even In the stand-alone case, it has been shown that every efficient function can be securely computed. However, in the setting of concurrent composition, broad impossibility results have been proven for the case where there is no honest majority ...

Keywords: cryptography, protocol composition, secure multiparty computation

20 Architectures for cryptography and security applications: Simulation models for side-



Kris Tiri, Ingrid Verbauwhede

channel information leaks

June 2005 Proceedings of the 42nd annual conference on Design automation DAC '05 **Publisher: ACM Press**

Full text available: pdf(244.28 KB) Additional Information: full citation, abstract, references, index terms

Small, embedded integrated circuits (ICs) such as smart cards are vulnerable to so-called side-channel attacks (SCAs). The attacker can gain information by monitoring the power consumption, execution time, electromagnetic radiation and other information that is leaked by the switching behavior of digital CMOS gates. Ever since power attacks have been introduced in 1999, many countermeasures have been proposed. Often a significant increase in security has been touted. We will show that in order t ...

Keywords: countermeasure, differential power analysis, encryption, security IC, sidechannel attack, simulation model, smart card

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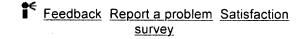
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1 Ad hoc networks and RFID (work in progress): Improved topology assumptions for



threshold cryptography in mobile ad hoc networks

Giovanni Di Crescenzo, Renwei Ge, Gonzalo R. Arce

November 2005 Proceedings of the 3rd ACM workshop on Security of ad hoc and sensor networks SASN '05

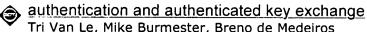
Publisher: ACM Press

Full text available: pdf(281.49 KB) Additional Information: full citation, abstract, references, index terms

Mobile Ad Hoc Networks (MANET), due to their lack of physical infrastructures or centralized authorities, pose a number of security challenges to a protocol designer. In particular, several typical application scenarios demand the design of protocols that cannot base their security on the existence of trusted parties or setup information, but rather need to leverage uniquely on assumptions limiting the corrupting power of the adversaries. This naturally defines security design and analysis parad ...

Keywords: distributed cryptography, mobile ad hoc networks, threshold cryptography, threshold signatures

2 RFID & watermarking: Universally composable and forward-secure RFID



Tri Van Le, Mike Burmester, Breno de Medeiros

March 2007 Proceedings of the 2nd ACM symposium on Information, computer and communications security ASIACCS '07

Publisher: ACM Press

Full text available: pdf(367.90 KB) Additional Information: full citation, abstract, references, index terms

Recently, a universally composable framework for RFID authentication protocols providing availability, anonymity, and authenticity was proposed. In this paper we extend that framework to address forward-security issues in the presence of key compromise. We also introduce new, provably secure, and highly practical protocols for anonymous authentication and key-exchange by RFID devices. The new protocols are lightweight, requiring only a pseudo-random bit generator. The new protocols satisfy forwar ...

Keywords: RFID authentication and key-exchange protocols, anonymity, forwardsecurity, universal composability

3 Web-based and Java-based simulation: Web-based simulation management: a webbased interface for storing and executing simulation models

Ashu Guru, Paul Savory, Robert Williams

December 2000 Proceedings of the 32nd conference on Winter simulation WSC '00

Publisher: Society for Computer Simulation International

Full text available: pdf(181.99 KB) Additional Information: full citation, abstract, references, citings

The dominance of the Internet in the development of information and communication technology has made Web-based distributed solutions increasingly attractive. Apart from providing other services, the World Wide Web is being looked upon as an environment for hosting modeling and simulation applications. SIMAN is a simulation language that allows users to simulate discrete and continuous systems. In this research, a web-based interface or toolkit has been developed for storing and executing SIMAN ...

4 Military applications: Security issues in high level architecture based distributed simulation

Asa Elkins, Jeffery W. Wilson, Denis Gracanin

December 2001 Proceedings of the 33nd conference on Winter simulation WSC '01 Publisher: IEEE Computer Society

Full text available: pdf(262.26 KB) Additional Information: full citation, abstract, references, index terms

The United States Department of Defense (DoD) has, over the past several years, emphasized the need to employ simulation based acquisition (SBA) in engineering and development. Distributed simulation introduces an information assurance challenge and details of a simulation must be guarded from unauthorized access. The High Level Architecture (HLA) and its Run-Time Interface (RTI) do not define support of mandatory access controls (MACs) or discretionary access controls (DACs) required to provide ...

5 Homeland security/emergency response: simulation for emergency management: Integrated simulation and gaming architecture for incident management training Sanjay Jain, Charles R. McLean

December 2005 Proceedings of the 37th conference on Winter simulation WSC '05 Publisher: Winter Simulation Conference

Full text available: pdf(249.50 KB) Additional Information: full citation, abstract, references

The simulation-based training systems that are available or under development today for incident management are typically focused on macro level sequence of events. A few systems targeted at individual responders are under development using a gaming environment. Separate uses of such systems provide disparate experiences to decision makers and individual responders. There is a need to provide common training experiences to these groups for better effectiveness. This paper presents a novel approa ...

Formal modeling of active network nodes using PVS

Cindy Kong, P. Alexander, Darryl Dieckman

August 2000 Proceedings of the third workshop on Formal methods in software practice FMSP '00

Publisher: ACM Press

Full text available: pdf(440.28 KB) Additional Information: full citation, abstract, references, index terms

Active Networks are a new type of networks where all elements are programmable. Active packets can contain fragments of code to be executed on intermediate nodes they pass through. Active nodes provide the necessary environment and resources for the packets to be processed. In giving the users the capability to program the network as they desire, there is an issue of security risks. This paper presents a formal model for an active node that can be used to specify and verify the correct oper ...



7 An optimally robust hybrid mix network

Markus Jakobsson, Ari Juels

August 2001 Proceedings of the twentieth annual ACM symposium on Principles of distributed computing PODC '01

Publisher: ACM Press

Full text available: pdf(858.02 KB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

We present a mix network that achieves efficient integration of public-key and symmetric-key operations. This *hybrid* mix network is capable of natural processing of arbitrarily long input elements, and is fast in both practical and asymptotic senses. While the overhead in the size of input elements is linear in the number of mix servers, it is quite small in practice. In contrast to previous hybrid constructions, ours has optimal robustness, that is, robustness against any minority coa ...

8 Cryptographic protocols/ network security: Security proofs for an efficient password-



based key exchange

Emmanuel Bresson, Olivier Chevassut, David Pointcheval

October 2003 Proceedings of the 10th ACM conference on Computer and communications security CCS '03

Publisher: ACM Press

Full text available: pdf(233.51 KB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

Password-based key exchange schemes are designed to provide entities communicating over a public network, and sharing a (short) password only, with a session key (e.g, the key is used for data integrity and/or confidentiality). The focus of the present paper is on the analysis of very efficient schemes that have been proposed to the IEEE P1363 Standard working group on password-based authenticated key-exchange methods, but which actual security was an open problem. We analyze the AuthA key excha ...

Keywords: key exchange, password-based authentication

9 Aurora: An Approach to High Throughput Parallel Simulation

Alfred Park, Richard M. Fujimoto

May 2006 Proceedings of the 20th Workshop on Principles of Advanced and Distributed Simulation PADS '06

Publisher: IEEE Computer Society

Full text available: pdf(333.08 KB) Additional Information: full citation, abstract, index terms

A master/worker paradigm for executing large-scale parallel discrete event simulation programs over networkenabled computational resources is proposed and evaluated. In contrast to conventional approaches to parallel simulation, a client/server architecture is proposed where clients (workers) repeatedly download state vectors of logical processes and associated message data from a server (master), perform simulation computations locally at the client, and then return the results back to the serv ...

10 Java-based query driven simulation environment

Rajesh S. Nair, John A. Miller, Zhiwei Zhang

November 1996 Proceedings of the 28th conference on Winter simulation WSC '96

Publisher: ACM Press, IEEE Computer Society

Full text available: pdf(744.69 KB) Additional Information: full citation, abstract, references, citings

The concept of Web-based simulation can be finally realized using Java. We have used Java to create a powerful simulation modeling library which is based on the process

interaction paradigm. Java threads make it easy for us to implement each process (or active entity) as a thread. Our library, JSIM, supports both simulation and animation thus rendering the model developer's job easier. An important component of our approach is that we integrate our Java Simulator with a Database Management Syste ...

11 Computing curricula 2001



Publisher: ACM Press

Full text available: pdf(613.63 KB)

1 html(2.78 KB)

Additional Information: full citation, references, citings, index terms

12 The Diesel Combustion Collaboratory: combustion researchers collaborating over the



Internet

Carmen M. Pancerella, Larry A. Rahn, Christine L. Yang

January 1999 Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM) Supercomputing '99

Publisher: ACM Press

Full text available: pdf(8.95 MB)

Additional Information: full citation, references, citings, index terms

13 Agent-based modeling and simulation: Simulation using software agents I: linking spatially explicit parallel continuous and discrete models

Boleslaw K. Szymanski, Gilbert Chen

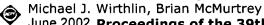
December 2000 Proceedings of the 32nd conference on Winter simulation WSC '00

Publisher: Society for Computer Simulation International

Full text available: pdf(223.71 KB) Additional Information: full citation, abstract, references

This paper advocates the use of mobile agents for linking simulations running on different computers. A Mobile Component approach is proposed to enhance reusability of existing simulations and to improve efficiency of component based simulations of complex systems. A basic unit of the mobile component simulation is a simulation server with a communication interface to mobile agents. Each mobile agent links and coordinates component's execution. We used this approach to implement a combined Lyme ...

14 Web and IP based design: IP delivery for FPGAs using Applets and JHDL



June 2002 Proceedings of the 39th conference on Design automation DAC '02

Publisher: ACM Press

Full text available: pdf(198.07 KB) Additional Information: full citation, abstract, references, index terms

This paper introduces an FPGA IP evaluation and delivery system that operates within Java applets. The use of such applets allows designers to create, evaluate, test, and obtain FPGA circuits directly within a web browser. Based on the JHDL design tool, these applets allow structural viewing, circuit simulation, and netlist generation of applicationspecific circuits. Applets can be customized to provide varying levels of IP visibility and functionality as needed by both customer and vendor.

Keywords: FPGA, JHDL, applet, intellectual property

15 Session 7A: New notions of security: achieving universal composability without trusted setup



Manoj Prabhakaran, Amit Sahai

June 2004 Proceedings of the thirty-sixth annual ACM symposium on Theory of computing STOC '04

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(253.45 KB) terms

We propose a modification to the framework of Universally Composable (UC) security [3]. Our new notion involves comparing the real protocol execution with an ideal execution involving ideal functionalities (just as in UC-security), but allowing the environment and adversary access to some super-polynomial computational power. We argue the meaningfulness of the new notion, which in particular subsumes many of the traditional notions of security. We generalize the Universal Composition theorem of ...

Keywords: environmental security, general composition, generalized environmental security, secure multi-party computation, secure protocols, simulation, universal composability

16 Three phase simulation in Java

Michael Pidd, R. A. Cassel

December 1998 Proceedings of the 30th conference on Winter simulation WSC '98

Publisher: IEEE Computer Society Press

Full text available: pdf(48.10 KB) Additional Information: full citation, references, citings, index terms

17 Reprint: MSIS 2006: model curriculum and guidelines for graduate degree programs





in information systems

John T. Gorgone, Paul Gray, Edward A. Stohr, Joseph S. Valacich, Rolf T. Wigand June 2006 ACM SIGCSE Bulletin, Volume 38 Issue 2

Publisher: ACM Press

Full text available: R pdf(868.32 KB) Additional Information: full citation, abstract, index terms

This article presents the MSIS 2006 Model Curriculum and Guidelines for Graduate Degree Programs in Information Systems. As with MSIS 2000 and its predecessors, the objective is to create a model for schools designing or revising an MS curriculum in Information Systems. The curriculum was designed by a joint committee of the Association for Information Systems and the Association for Computing Machinery. MSIS2006 is a major update of MSIS 2000. Features include increasing the number of required c ...

Keywords: MS career tracks, MS course outlines, MS curriculum

18 General applications: Complex and interconnected systems: optimistic parallel simulation of a large-scale view storage system

Garrett Yaun, Christopher D. Carothers, Sibel Adali, David Spooner

December 2001 Proceedings of the 33nd conference on Winter simulation WSC '01

Publisher: IEEE Computer Society

Full text available: 7 pdf(139.73 KB) Additional Information: full citation, abstract, references, index terms

In this paper we present the design and implementation of a complex view storage system model that is suitable for execution on a optimistic parallel simulation engine. What is unique over other optimistic systems is that reverse computation as opposed to state-saving is used to support the rollback mechanism. In this model, a hierarchy of view storage servers are connected to an array of client-side local disks. The term view refers to the output or result of a query made on the part of ...

19 Advances in Model-Based Testing (A-MOST 2005): A simulation model of a multi-



server EJB system

David Mc Guinness, Liam Murphy

May 2005 ACM SIGSOFT Software Engineering Notes, Proceedings of the first international workshop on Advances in model-based testing A-MOST '05,

Publisher: ACM Press

Full text available: pdf(161.42 KB) Additional Information: full citation, abstract, references, index terms

Despite the fact that EJB (Enterprise Java Beans) is a widely used technology, research in the area of performance modelling of EJB application servers is quite sparse. This paper will describe how Workbench™, an advanced simulation modelling tool, can be used to build a scalable model of a multi-server EJB system that allows users to input variables that describe interactions and their constituent methods, as well as system parameters. The model will output the average time for each given ...

²⁰ Computer security: Software security vulnerability testing in hostile environments



Herbert H. Thompson, James A. Whittaker, Florence E. Mottay

March 2002 Proceedings of the 2002 ACM symposium on Applied computing SAC '02 Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(547.79 KB) terms

Traditional Black box software testing can be effective at exposing some classes of software failures. Security class failures, however, do not tend to manifest readily using these techniques. The problem is that many security failures occur in stressed environments, which appear in the field, but are often neglected during testing because of the difficulty to simulate these conditions. Software can only be considered secure if it behaves securely under all operating environments. Hostile enviro ...

Keywords: fault injection, software defect, software failure, software security, software testing

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P Michiardi, R Molva - European Wireless Conference, 2002 - eurecom.fr ... taken, the **simulation** results showed that network operation and ... that only forces the correct **execution** of the ... rising any suspects in the **security** mechanism. ... Cited by 56 - Related Articles - View as HTML - Web Search

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C McLean, F Riddick - Proceedings of the 32nd conference on Winter simulation, 2000 - portal.acm.org

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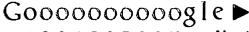
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R Hubbold, D Xiao, S Gibson - Presented at the 3rd Eurographics Workshop on Virtual ..., 1996 - cs.man.ac.uk

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MD Smith, Computer Systems Laboratory, Stanford ... - 1991 - eecs.harvard.edu ... the pipe to a disk le. This action produces the disk le that can be later read as the input trace stream. The code to read in this ... Cited by 155 - Related Articles - View as HTML - Web Search - Library Search

[BOOK] Input/output behavior of supercomputing applications - all 23 versions » EL Miller, RH Katz - 1991 - ACM Press New York, NY, USA Page 1. Input/Output Behavior of Supercomputing Applications Ethan L. Miller Randy H. Katz ... Cray Y-MP. We chose to trace applications with high ... Cited by 77 - Related Articles - Web Search - Library Search

[PS] Incomplete trace data and trace driven simulation - all 6 versions »

JK Flanagan, BE Nelson, JK Archibald, K Grimsrud - Proc. of the International Workshop on Modeling, Analysis ... - pel.cs.byu.edu
... method of simulation may provide useful estimates of system performance, but is dependent on the qual- ity of both the simulation model and the input trace data ...

Cited by 12 - Related Articles - View as HTML - Web Search - BL Direct

... Input to Cholinergic Forebrain Neurons: An Ultrastructural Study Using Retrograde Tracing of HRP and ... - all 3 versions »

L Zh3RSZKY, L HEIMER, F ECKENSTEIN, C LERANTH - THE JOURNAL OF COMPARATIVE NEUROLOGY, 1986 - doi.wiley.com

... 250: 282-295 (1986) GABAergic Input to Cholinergic Forebrain Neurons: An Ultrastructural Study Using Retrograde Tracing of HRP and Double Immunolabeling ...

Cited by 37 - Related Articles - Web Search

WordNet:: Similarity-Measuring the Relatedness of Concepts - all 10 versions

T Pedersen, S Patwardhan, J Michelizzi - Proceedings of the Nineteenth National Conference on ..., 2004 - acl.ldc.upenn.edu

... the super class of all modules, and pro- vides general services used by all of the measures such as validation of synset identifier input, tracing, and caching ... Cited by 85 - Related Articles - View as HTML - Web Search - BL Direct

... coeruleus in control of melanotrope cells of Xenopus laevis: a retrograde and anterograde tracing ... - all 2 versions »

R Tuinhof, C Artero, A Fasolo, MF Franzoni, HJ Ten ... - Neuroscience, 1994 ncbi.nlm.nih.gov

Involvement of retinohypothalamic input, suprachiasmatic nucleus, magnocellular nucleus and locus ... of Xenopus laevis: a retrograde and anterograde tracing study ... Cited by 31 - Related Articles - Web Search - BL Direct

Mining specifications - all 14 versions »

G Ammons, R Bodík, JR Larus - Proceedings of the 29th ACM SIGPLAN-SIGACT symposium on ..., 2002 - portal.acm.org

... 3. TRACING AND FLOW DEPENDENCE ANNOTATION This section describes the tracing and

flow dependence annota- tion that produce the input to the scenario extractor. ... Cited by 124 - Related Articles - Web Search - BL Direct

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L3	2278	726/3,4,21.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/06/07 14:40
L4	2041	726/3,4.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/06/07 14:40
L5	684	726/3.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND .	ON	2007/06/07 14:41
L6	6	3 allowable adj action	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ÄND	ON	2007/06/07 14:42
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L10	187	8 server near2 (message or instruction or code)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/06/07 15:29

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L12	0	(simulated adj execution client server message action allowable). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON .	2007/06/07 15:30
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